

# KSD-mSATA.5-XXMJ

**(Based on NAND Flash SATA MiniPCle SSD)**

**Datasheet**

**Rev.1.0**

Apr. 2011

Issue:

Check:

Approve:

NOTE: INFORMATION IN THIS PRODUCT SPECIFICATION IS SUBJECT TO CHANGE AT ANYTIME WITHOUT NOTICE, ALL PRODUCT SPECIFICATIONS ARE PROVIDED FOR REFERENCE ONLY. TO ANY INTELLECTUAL PROPERTY RIGHTS IN KINGSPEC PRODUCT OR TECHNOLOGY. ALL INFORMATION IN THIS DOCUMENT IS PROVIDED.

## Table of Contents

<b>1.</b>	<b>Introduction.....</b>	<b>3</b>
	1.1 Overview.....	3
	1.2 Feature.....	3
<b>2.</b>	<b>Block Diagram.....</b>	<b>4</b>
<b>3.</b>	<b>Product Specifications.....</b>	<b>5</b>
	3.1 Physical Specifications.....	5
	3.2 Interface.....	6
	3.3 Performance.....	6
<b>4.</b>	<b>Interface Description .....</b>	<b>7</b>
	4.1 Pin Description.....	7
<b>5.</b>	<b>Product Trait.....</b>	<b>8</b>
	5.1 Environment Specification.....	8
	5.2 Power Specification.....	8
	5.3 Reliability Specification.....	9
<b>6.</b>	<b>Electrical Specification.....</b>	<b>10</b>
<b>7.</b>	<b>Command Description.....</b>	<b>11</b>
	7.1 Support ATA Command.....	11
	7.2 ATA COMMAND SPECIFICATIONS.....	11
<b>8.</b>	<b>Ordering Information.....</b>	<b>15</b>

## 1. Introduction

### 1.1 Overview

KingSpec's SATA Mini PCIe SSD (Solid State Drive) is a high performance and high reliability storage device based on NAND Flash technology that designed to solve the bottleneck of computing system by traditional hard disk drives. KingSpec's SATA Mini PCIe SSD doesn't have a moving parts and it has a same host interface and same physical dimension with Hard Disk Drive, So it can be drop-in replaced with the hard disk drives without anything. With a high performance and low power consumption, KingSpec's SATA Mini PCIe SSD can be a good storage device for NB and Tabletop PC ,

KingSpec's SATA Mini PCIe SSD purely consists of semiconductor devices and NAND flash memories, which give rugged features against shock and vibration use in extreme environment such as industrial PC an increased MTBF. Further more, KingSpec's SATA Mini PCIe SSD has highly advanced flash memory management algorithm to guarantee higher performance and data integrity.

### 1.2 Feature

- Performance  
External Transfer Rate(Host Transfer Rate):300MB/s  
Sequential Data Read/Write: 71/15 MB/s; 117/33 MB/s; 145/60 MB/s; 153/102 MB/s;  
Form factor: 50MM(L) X 30MM(W)
- Interface standard: Serial ATA Revision 2.6 and Universal Serial Bus Specification Revision 2.0
- Density: 8GB;16GB;32GB;64GB;
- Input voltage: 3.3V ( $\pm 5\%$ )
- Commercial operating temperature range form 0 to +75°C
- Flash management algorithm: static and dynamic wear-leveling, bad block management algorithm
- Supports dynamic power management and SMART (Self-Monitoring, Analysis and Reporting Technology), NCQ;
- ECC (Error Correction Code): BCH ECC 16bits or 24bits in 1024Bytes
- Write endurance: >80 years @ 100GB write/ day(32GB)
- Read endurance: unlimited
- Data retention: 10 years
- MTBF:1,500,000 Hours

## 2. Block Diagram

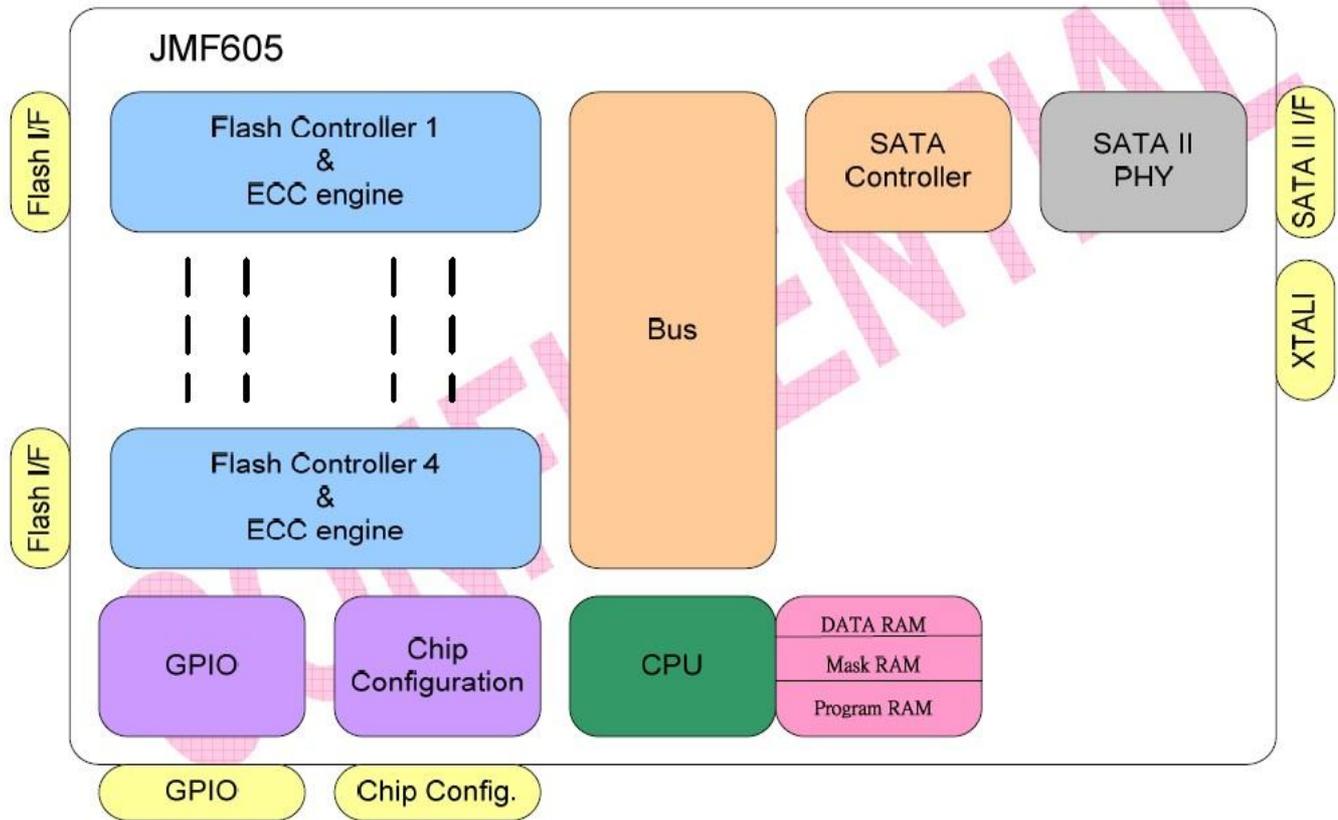


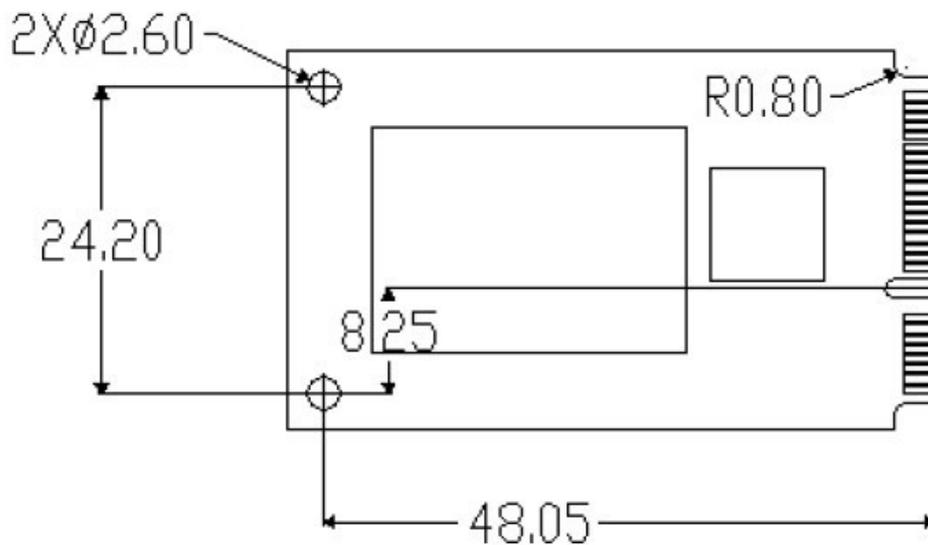
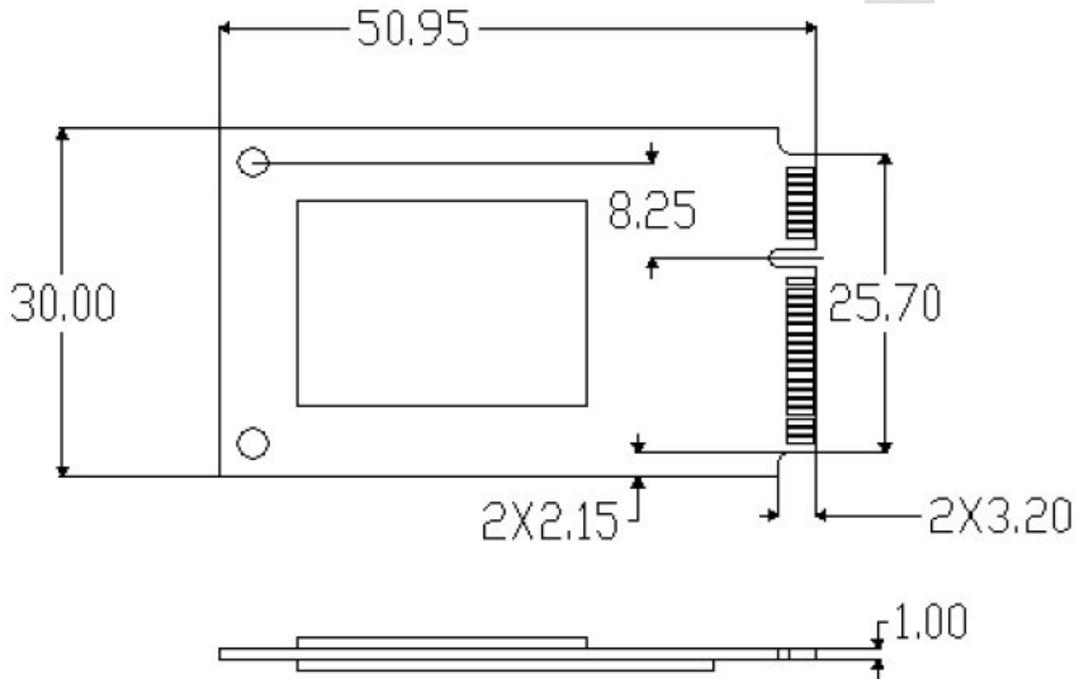
Figure 1: JMF605 Block Diagram

### 3. Product Specifications

#### 3.1 Physical Specifications

**Table 1 Physical dimensions and weight**

Model	Height (mm)	Width (mm)	Length (mm)	Weight(g)
8~64GB;	3.2+/-0.25	30.1+/-0.25	51+/-0.25	TBD



### 3.2 Interface

The interface of KingSpec's SATA Mini PCIe complies with the standard serial ATA revision 2.6:

- ① Host Transfer Rate is 300MB/s(3.0Gb/s)
- ② PIO mode 0,1,2,3,4
- ③ DMA mode 0,1,2
- ④ UDMA mode 0,1,2,3,4,5,6

### 3.3 Performance Testing

#### 3.3.1 Maximum Data Transfer Rate:

- Maximum sustained read: 159MB/s
- Maximum sustained write: 106MB/s

**Note:**

- 1) .Test PC: **CPU:** Intel® Core™ i3 Duo CPU 540 @ 3.07GHz,  
**Memory:**Kingston KVR800D2N5/2G,  
**Chipset:** Intel® H55 Express Chipset  
**OS:** Microsoft Windows 7 Professional 64-bit  
**BIOS Settings:** AHCI Mode
- 2) .Test Program: HD Tune pro 3.5
- 3) .Test Drive: KSM-mSATA.5-XXXMJ

#### 3.3.2 IO Performance:

**Tabel 2 IO Performance Values For 50X30 miniPCIE" SATA MLC SSD**

Access Type	IOPS Operation unit size	
	512Byte	4KByte
Sequential Read	10462	6090
Sequential Write	3791	13465
Random Read	10247	5901
Random Write	54	68

**Note:**

- 1) . Test PC: **CPU:** Intel® Core™ i3 Duo CPU 540 @ 3.07GHz,  
**Memory:**Kingston KVR800D2N5/2G,  
**Chipset:** Intel® H55 Express Chipset  
**OS:** Microsoft Windows 7 Professional 64-bit  
**BIOS Settings:** AHCI Mode
- 2) .Test Program: IOMeter 2006.07.27
- 3) .Test Drive: KSM-mSATA.5-XXXMJ

#### 3.3.3 Access Time:

Random access time: 0.2 msec

**Note:**

- 1) . Test PC: **CPU:** Intel® Core™ i3 Duo CPU 540 @ 3.07GHz,  
**Memory:**Kingston KVR800D2N5/2G,  
**Chipset:** Intel® H55 Express Chipset  
**OS:** Microsoft Windows 7 Professional 64-bit  
**BIOS Settings:** AHCI Mode
- 2) .Test Program: HDTech 3.0.1
- 3) .Test Drive: KSM-mSATA.5-XXXMJ

## 4. Interface Description

### 4.1 Pin Description

Connector Pin out			
Pin #	Signal Name	Pin #	Signal Name
51	Presence Detection	52	+3.3V
49	DA/DSS	50	GND
47	Reserved	48	Reserved
45	Reserved	46	Reserved
43	GND	44	Reserved
41	+3.3V	42	Reserved
39	+3.3V	40	GND
37	GND	38	Reserved
35	GND	36	Reserved
33	PERp0	34	GND
31	PERn0	32	Reserved
29	GND	30	Reserved
27	GND	28	Reserved
25	PETn0	26	GND
23	PETp0	24	+3.3V
21	GND	22	Reserved
19	Reserved	20	Reserved
17	Reserved	18	GND
Mechanical Key			
15	GND	16	Reserved
13	Reserved	14	Reserved
11	Reserved	12	Reserved
9	GND	10	Reserved
7	Reserved	8	Reserved
5	Reserved	6	Reserved
3	Reserved	4	GND
1	Reserved	2	+3.3V

**5.Product Trait****5.1 Environment Specification****Table 4 Environmental Specifications**

Features	Operating
Temperature	0~75°C
Humidity	90%

**5.2 Power Specification****Recommended DC Operating Voltage:3.0V~3.4V****Table 5 Power Consumption**

Parameter		Typical	Unit
Standby		0.5	W
Idel		0.65	W
4KByte Sample Data	Sequential Read	1.2	W
	Sequential Write	1.5	W
	Random Read	1.2	W
	Random Write	1.7	W
512KByte Sample Data	Sequential Read	1.05	W
	Sequential Write	1.6	W
	Random Read	1.05	W
	Random Write	1.65	W

**Note:**

1. The power consumption can differ depending on the disk capacity and the type of flash memory being used
2. The test drive: KSM-mSATA.5-XXXMJ
3. Supply voltage:3.3V+/-5%

### **5.3 Reliability Specification**

#### **Wear-leveling**

KingSpec's SSD support both static and dynamic wear-leveling, These two algorithms guarantee all type of flash memory at same level of erase cycles to improve lifetime limitation of NAND based storage

#### **Endurance**

Write endurance: >80 years @ 100GB write/ day(32GB)

Read endurance: unlimited

#### **ECC**

ECC (Error Correction Code): BCH ECC 16bits or 24bits in 1024Bytes

#### **Bad block management algorithm**

This algorithm replaces bad blocks with new ones from available spares on media error conditions.

#### **MTBF**

MTBF(Mean Time between Failures) of KingSpec's SSD: 1, 500, 000 hours

#### **Data retention**

Data retention at 25°C of KingSpec's SSD:>10 years

## 6. Electrical Specification

### 10.1 Absolute Maximum Rating

Parameter	Symbol	Condition	Min	Max	Unit
Analog power supply	AVDDH		-0.5	6	V
Digital I/O power supply	DVDD		-0.5	6	V
Digital I/O input voltage	V <sub>I(D)</sub>		-0.4	DVDD+0.4	V
Storage temperature	T <sub>STORAGE</sub>		-55	140	°C

### 10.2 Recommended Power Supply Operation Conditions and Temperature

Parameter	Symbol	Condition	Min	Typical	Max	Unit
Operation digital power supply	PV33		3.0	3.3	3.6	V
	D1V2		-5%	1.2	+5%	V
Operation analog power supply	ASV33		3.0	3.3	3.6	V
	ASV12		-5%	1.2	+5%	V
	AVDDH		3.0	3.3	3.6	V
Ambient operation temperature	T <sub>A</sub>	For commercial spec.	0		70	°C
Ambient operation temperature	T <sub>A</sub>	For industry spec.	-40		85	°C
Junction temperature	T <sub>J</sub>				125	°C
Case operation temperature	T <sub>c</sub>	For commercial spec and base on T <sub>A</sub>			85	°C
Case operation temperature	T <sub>c</sub>	For industry spec and base on T <sub>A</sub>			100	°C
TFBGA 130 ball 9X9	θJC					°C/W
TFBGA 130 ball 9X9	θJA					°C/W

### 10.3 Recommended External Clock Source Conditions

Parameter	Symbol	Condition	Min	Typical	Max	Unit
External reference clock				30		MHz
Clock Duty Cycle			45	50	55	%

### 10.4 Power Supply DC Characteristics (SATA Idle mode)

Parameter	Symbol	Condition	Min	Typical	Max	Unit
Digital power supply	I <sub>PV33</sub>	3.3V		7	14	mA
	I <sub>DV12</sub>	1.2V		272	373	mA
SATA Analog Power Supply	I <sub>ASV33</sub>	3.3V		34	41	mA
	I <sub>ASV12</sub>	1.2V		61	68	mA

### 10.5 I/O DC Characteristics

Parameter	Symbol	Condition	Min	Typical	Max	Unit
Input low voltage	V <sub>IL</sub>				0.8	V
Input high voltage	V <sub>IH</sub>		2.4			V
Output low voltage	V <sub>OL</sub>		1.5			V
Output high voltage	V <sub>OH</sub>				3.3	V

## 7.Command Descriptions

### 7.1 Support ATA Command

Command Name	Code (Hex)	Command Name	Code (Hex)
CHECK POWER MODE	E5h	SECURITY ERASE UNIT	F4h
EXECUTE DIAGNOSTICS	90h	SECURITY FREEZE LOCK	F5h
FLUSH CACHE	E7h	SECURITY SET PASSWORD	F1h
IDENTIFY DEVICE	ECh	SECURITY UNLOCK	F2h
IDLE	E3h	SEEK	7xh
IDLE IMMEDIATE	E1h	SET FEATURES	EFh
INITIALIZE DEVICE PARAMETERS	91h	SET MULTIPLE MODE	C6h
READ DMA	C8h or C9h	SLEEP	E6h
READ MULTIPLE	C4h	SMART	B0h
READ SECTOR(S)	20h or 21h	STANDBY	E2h
READ VERIFY SECTOR(S)	40h or 41h	STANDBY IMMEDIATE	E0h
RECALIBRATE	10h	WRITE DMA	CAh or CBh
SECURITY DISABLE PASSWORD	F6h	WRITE MULTIPLE	C5h
SECURITY ERASE PREPARE	F3h	WRITE SECTOR(S)	30h or 31h

### 7.2 ATA COMMAND SPECIFICATIONS

#### CHECK POWER MODE (E5h)

The host can use this command to determine the current power management mode.

Sector Count result value-

00h – device is in Standby mode

80h – device is in Idle mode

FFh – device is in Active mode or Idle mode

#### EXECUTE DIAGNOSTICS (90h)

This command performs the internal diagnostic tests implemented by the drive. See ERROR register for diagnostic codes.

#### FLUSH CACHE (E7h)

This command is used by the host to request the device to flush the write cache. If there is data in the write cache, that data shall be written to the media. The BSY bit shall remain set to one until all data has been successfully written or an error occurs.

#### IDENTIFY DEVICE (ECh)

This commands read out 512Bytes of drive parameter information. Parameter Information consists of the arrangement and value as shown in the following table. This command enables the host to receive the Identify Drive Information from the device.

#### IDLE (E3h)

This command causes the device to set BSY, enter the Idle mode, clear BSY and generate an interrupt. If sector count is non-zero, the automatic power down mode is enabled. If the sector count is zero, the automatic power mode is disabled.

ADD: 3rd/F., 4th Block, Tongfuyu Ind. Park, Tanglang, Xili, Nanshan, Shenzhen, China

**IDLE IMMEDIATE (E1h)**

This command causes the device to set BSY, enter the Idle(Read) mode, clear BSY and generate an interrupt. If sector count is non-zero, the automatic power down mode is enabled. If the sector count is zero, the automatic power mode is disabled.

**INITIALIZE DEVICE PARAMETERS (91h)**

This command enables the host to set the number of sectors per track and the number of tracks per heads.

**READ DMA (C8h)**

Reads data from sectors during Ultra DMA and Multiword DMA transfer. Use the SET FEATURES command to specify the mode value. A sector count of zero requests 256 sectors.

**READ MULTIPLE (C4h)**

This command performs similarly to the Read Sectors command. Interrupts are not generated on each sector, but on the transfer of a block which contains the number of sectors defined by a Set Multiple command.

**READ SECTOR(S) (20h/21h)**

This command reads 1 to 256 sectors as specified in the Sector Count register from sectors which is set by Sector number register. A sector count of 0 requests 256 sectors. The transfer beings specified in the Sector Number register.

**READ VERIFY SECTOR(S) (40h/41h)**

This command verifies one or more sectors on the drive by transferring data from the flash media to the data buffer in the drive and verifying that the ECC is correct. This command is identical to the Read Sectors command, except that DRQ is never set and no data is transferred to the host.

**RECALIBRATE (10h)**

The current drive performs no processing if it receives this command. It is supported for backward compatibility with previous devices.

**SECURITY DISABLE PASSWORD (F6h)**

Disables any previously set user password and cancels the lock. The host transfers 512 bytes of data, as shown in the following table, to the drive. The transferred data contains a user or master password, which the drive compares with the saved password. If they match, the drive cancels the lock. The master password is still saved. It is re-enabled by issuing the SECURITY SET PASSWORD command to re-set a user password.

**SECURITY ERASE PREPARE (F3h)**

This command shall be issued immediately before the Security Erase Unit command to enable erasing and unlocking. This command prevents accidental loss of data on the drive.

**SECURITY ERASE UNIT (F4h)**

The host uses this command to transfer 512 bytes of data, as shown in the following table, to the drive. The transferred data contains a user or master password, which the drive compares with the saved password. If they match, the drive deletes user data, disables the user password, and cancels the lock. The master password is still saved. It is re-enabled by issuing the SECURITY SET

ADD: 3rd/F., 4th Block, Tongfuyu Ind. Park, Tanglang, Xili, Nanshan, Shenzhen, China

PASSWORD command to re-set a user password.

**SECURITY FREEZE LOCK (F5h)**

Causes the drive to enter Frozen mode. Once this command has been executed, the following commands to update a lock result in the

Aborted Command error:

- SECURITY SET PASSWORD
- SECURITY UNLOCK
- SECURITY DISABLE PASSWORD
- SECURITY ERASE PREPARE
- SECURITY ERASE UNIT

The drive exits from Frozen mode upon a power-off or hard reset. If the SECURITY FREEZE LOCK command is issued when the drive is placed in Frozen mode, the drive executes the command, staying in Frozen mode.

**SECURITY SET PASSWORD (F1h)**

This command set user password or master password. The host outputs sector data with PIO data-out protocol to indicate the information defined in the following table.

**SECURITY UNLOCK (F2h)**

This command disable LOCKED MODE of the device. This command transfers 512 bytes of data from the host with PIO data-out protocol. The following table defines the content of this information.

**SEEK (7xh)**

This command is effectively a NOP command to the device although it does perform a range check.

**SET FEATURES (EFh)**

This command set parameter to Features register and set drive's operation. For transfer mode, parameter is set to Sector Count register. This command is used by the host to establish or select certain features.

Features register Value and settable operating mode

Value	Function
02h	Enable write cache
03h	Set transfer mode based on value in Sector Count register.
55h	Disable read look-ahead feature
82h	Disable write cache
AAh	Enable read look-ahead feature

**SET MULTIPLE MODE (C6h)**

This command enables the device to perform READ MULTIPLE and WRITE MULTIPLE operations and establishes the block count for these commands.

**SLEEP (E6h)**

This command causes the device to set BSY, enter the Sleep mode, clear BSY and generate an

interrupt.

**SMART Function Set (B0h)**

Performs different processing required for predicting device failures, according to the subcommand specified in the Features register. If the Features register contains an unsupported value, the Aborted Command error is returned. If the SMART function is disabled, any subcommand other than SMART ENABLE OPERATIONS results in the Aborted Command error.

**STANDBY (E2h)**

This command causes the device to set BSY, enter the Sleep mode (which corresponds to the ATA!Standby Mode), clear BSY and return the interrupt immediately.

**STANDBY IMMEDIATE (E0h)**

This command causes the drive to set BSY, enter the Sleep mode (which corresponds to the ATA!Standby Mode), clear BSY and return the interrupt immediately.

**WRITE DMA (CAh)**

Write data to sectors during Ultra DMA and Multiword DMA transfer. Use the SET FEATURES command to specify the mode value.

**WRITE MULTIPLE (C5h)**

This command is similar to the Write Sectors command. Interrupts are not presented on each sector, but on the transfer of a block which contains the number of sectors defined by Set Multiple command.

**WRITE SECTOR(S) (30h/31h)**

Write data to a specified number of sectors (1 to 256, as specified with the Sector Count register) from the specified address. Specify "00h"– to write 256 sectors

## 8. Ordering Information

### **KSM-mSATA.5-XXXMJ**

**KSD:** Kingspec SSD

**mSATA:** SATA Mini PCIe Interface

**.5:** IC Revision is 605

**XXX:** Density

<b>XXX</b>	<b>Density</b>
<b>008</b>	<b>8GB</b>
<b>016</b>	<b>16GB</b>
<b>032</b>	<b>32GB</b>
<b>064</b>	<b>64GB</b>

**M:** Based on MLC NAND Flash

**J:** SSD Controller is JMicron